$\sim$  $\infty$ Power-combiner Control circuit Delay circuit adjustor Vector 18 n  $\infty$ `ത 9 circuit Delay Vector adjustor  $\Omega^{\checkmark}$ 9 Power splitter detection circuit 5 9 Signal level

Fig. 2

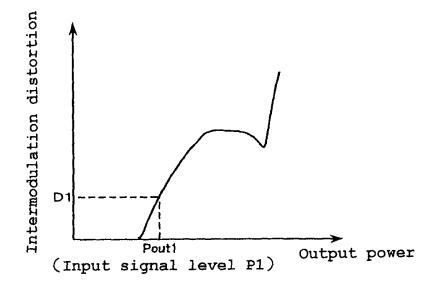
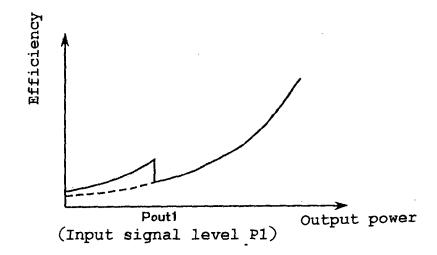
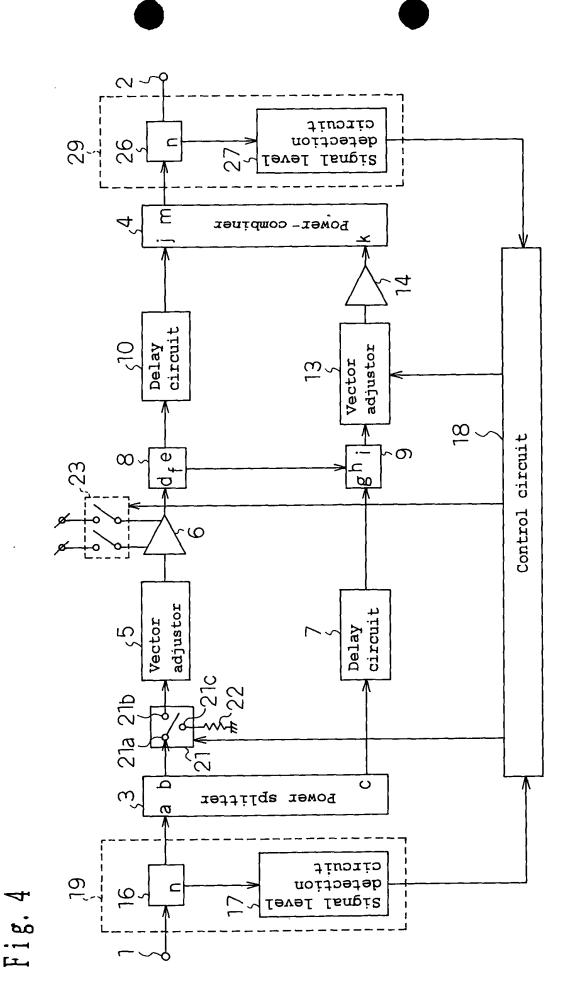
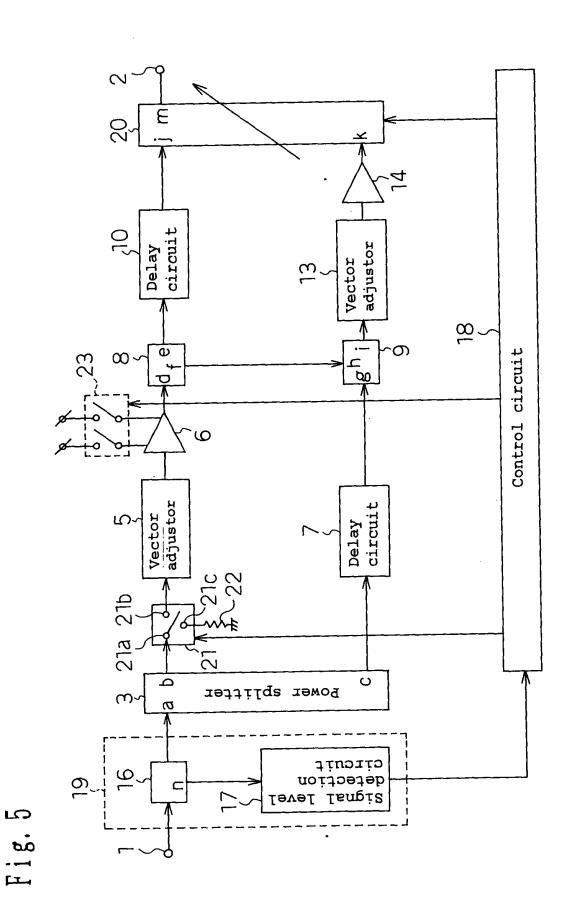
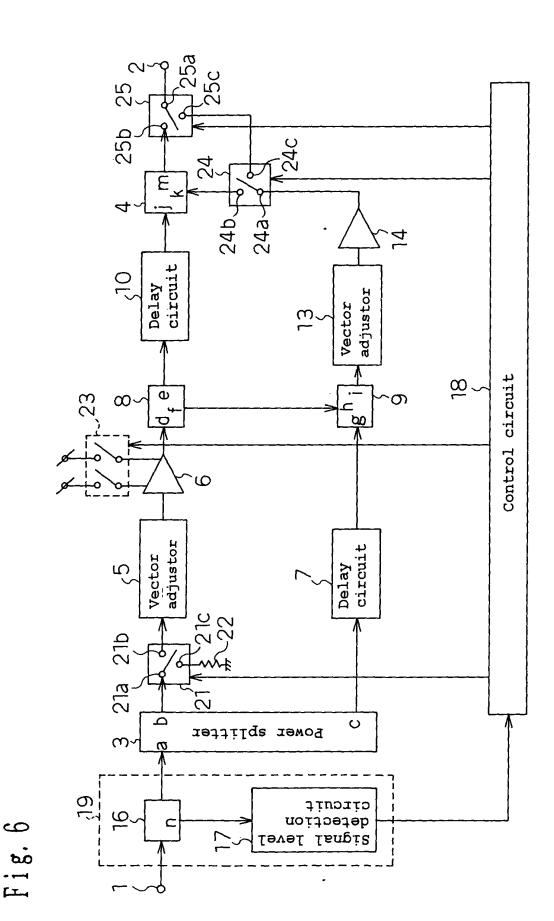


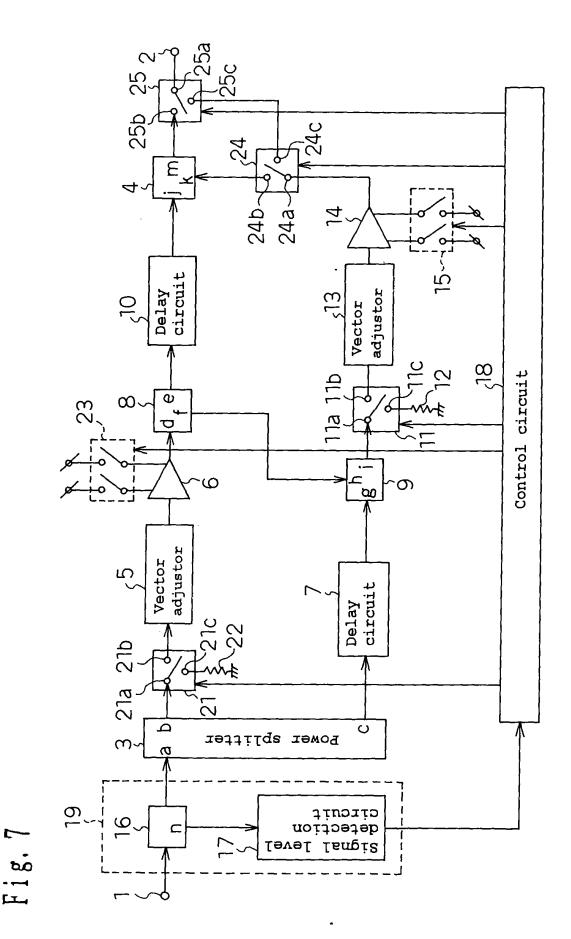
Fig. 3











 $\sim$ 53

Signal level detection circuit

Control

Н . . .

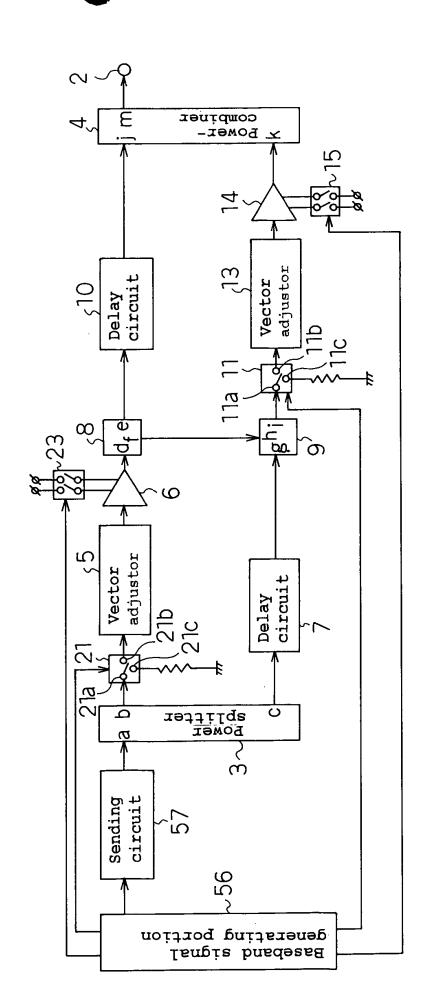
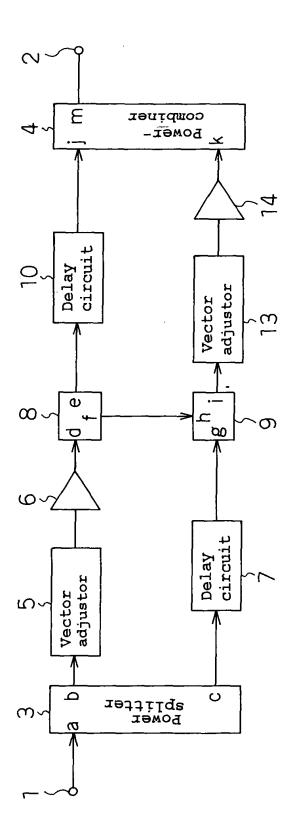
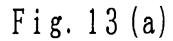
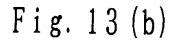


Fig. 12







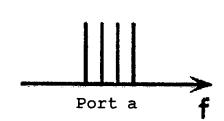


Fig. 13 (c)

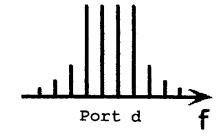


Fig. 13 (d)

